

Amendments to the Claims

1. (CURRENTLY AMENDED) A LC-Display with n gate drivers (~~GD~~) and a source drivers (~~SD~~) for driving a Display with dots arranged in x rows (~~R_x~~) and y columns (~~C_y~~), the gate driver (~~GD_n~~) has several output stages (~~OUT_x~~) for driving the gate lines (~~GL_y~~) of the display, characterised in that, an additional voltage line (~~V_{Lclean}~~) is provided, which is coupled to the output stages (~~OUT_x~~) of the gate driver (~~GD_n~~).
2. (CURRENTLY AMENDED) Display as claimed in claim 1, whereas the output stage is provided with a PMOS and two NMOS transistors and the PMOS transistor (~~MP1~~) is arranged between the supply line V_H and the output (~~OUT_x~~) of the output stage and the first NMOS transistor M_{N1} is arranged between the supply line (~~V_L~~) and the output (~~OUT_x~~) of the output stage and the second NMOS transistor (~~M_{N2}~~) is arranged between the supply line (~~V_{Lclean}~~) and the output (~~OUT_x~~) of the output stage.
3. (CURRENTLY AMENDED) Display as claimed in claim 1, whereas the additional supply line (~~V_{Lclean}~~) is routed over a separate track from V_L-potential.
4. (CURRENTLY AMENDED) Display as claimed in claim 1, whereas the track of the supply line (~~V_L~~) and the track of the supply line (~~V_{Lclean}~~) are coupled to the same supply level.
5. (CURRENTLY AMENDED) Display as claimed in claim 1, whereas the track of the supply line (~~V_L~~) and the track of the *supply* line (~~V_{Lclean}~~) are connected together in a location where the track impedance to the supply circuit's output is low.
6. (CURRENTLY AMENDED) Method for driving a display with n gate drivers (~~GD_n~~) and at least one source driver (~~SD~~), whereas dots are arranged in x rows (~~R_x~~) and y columns (~~C_y~~), the gate driver (~~GD_n~~) has several output stages (~~OUT_x~~) for driving gate lines (~~GL_y~~) of the display and a capacitance (~~C_{st}~~) of the selected gate line (~~GL_y~~) is connected to the previous gate line (~~GL_{y-1}~~) characterised in that, an

additional supply line (~~V_{le}~~) of the output stage for row (~~GL_y~~) is activated when the row (~~GL_{y+1}~~) is activated.

7. (CURRENTLY AMENDED) Method for driving a display with n gate drivers (~~GD~~) and a source driver (~~SD~~), whereas dots are arranged in x rows (~~R_x~~) and y columns (~~C_y~~), the gate driver (~~GD_n~~) has several output stages (~~OUT_x~~) for driving the gate lines (~~GL_y~~) of the display and a capacitance (~~C_{st}~~) of the selected gate line (~~GL_y~~) is connected to the next gate line (~~GL_{y+1}~~), characterised in that, an additional supply line (~~V_{le}~~) of the output stage for row (~~GL_y~~) is activated when (~~GL_{y+1}~~) is activated.